

General architecture

The Mirocom Broadband Engine (M/BE) is a low-power heterogeneous system-on-chip (SoC) aimed at targeting embedded systems as well as general purpose applications with moderate resource utilization.

Terminology

APE):

An application processing element (APE) is a RISC-V core / unit of execution that is capable of executing MISA instructions from memory.

SPE):

A synergistic processing element (SPE) is a specialized co-processor used for vectored and floating point operations and is connected to the APEs through the element interconnect bus (EIB).

EIB):

An element interconnect bus connects SPEs and APEs.

MISA):

The Mirocom Instruction Set Architecture (MISA) is the native instruction set understood by APEs.

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